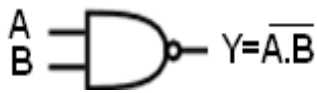


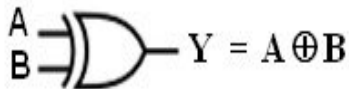
IC 7400 (NAND)



TRUTH TABLE (NAND)

A	B	$Y = \overline{A \cdot B}$
0	0	
0	1	
1	0	
1	1	

IC 7486 (EX-OR)



TRUTH TABLE (EX-OR)

A	B	$Y = A \oplus B$
0	0	
0	1	
1	0	
1	1	

**CALCULATION:
OR GATE**

INPUT A	INPUT B	OUTPUT $Y = A+B$
0	0	
0	1	
1	0	
1	1	

AND GATE

INPUT A	INPUT B	OUTPUT $Y = A \cdot B$
0	0	
0	1	
1	0	
1	1	

NOT GATE

INPUT A	OUTPUT $Y = \overline{A}$
0	
1	

NOR GATE

INPUT A	INPUT B	OUTPUT $Y = \overline{A + B}$
0	0	
0	1	
1	0	
1	1	

NAND GATE

INPUT A	INPUT B	OUTPUT $Y = \overline{A \cdot B}$
0	0	
0	1	
1	0	
1	1	

EX- OR GATE

INPUT A	INPUT B	OUTPUT $Y = A \oplus B$
0	0	
0	1	
1	0	
1	1	

RESULT:

The performance of Digital Gates OR, AND, NOT, NAND, NOR and EX-OR are verified using IC Chips